

A NEW APPROACH FOR NONLINEAR ANALOG CIRCUITS ANALYSIS

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This paper presents a new version of the hybrid method for nonlinear resistive circuit analysis. The approach for the generation of the hybrid equation is the separation of linear and nonlinear part of the circuit.

The efficiency comes from reducing computing time and memory. The main advantage is to evaluate, only once, at the beginning of the iteration process those parameters in the circuit equations that depend on the linear element terms.

Keywords: hybrid equations, Maple, analog circuit, resistive circuit

1. Introduction

There are several types of equations that describe nonlinear resistive circuits. The nonlinear circuits are described by a several types of equations. Among them, hybrid equations [1-3] are frequent used in the theoretical study of nonlinear circuits because they have an easily analyzed structure. Hybrid equations present also the advantage of numerical analysis of circuits because they consist of a small number of variables and are separable. However, the hybrid equations are rarely used in practical applications because their formulation is quite difficult [3]. This paper considers new approaches for analysis a non-linear circuits: one using hybrid equations, Maplesoft and second one using Matlab for Chua's circuit analysis.

2. Description of algorithm for formulation of hybrid equations

Hybrid equations have an easy-to-analyze structure and have the advantage that they are separable and consist of a relatively small number of variables. Because of these points hybrid equations are applied in the theoretical study of nonlinear resistive circuits.

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Analog circuits must meet certain conditions in order to formulate hybrid equations, the main one is that the circuit should allow the existence of a normal tree. Normal tree is a tree with restrictions that satisfy the following conditions in this order of priority,[8] :

- a) Contains all independent ideal sources and voltage controlled
- b) all nonlinear items controlled in voltage
- c) contains a maximum possible number of linear resistor
- d) contains no ideal independent source or controlled by current and any nonlinear element that is current controlled
- e) controlled dimensions of the controlled sources are associated with linear resistors or independent variables.

The hybrid method allows in the circuit a mixture of voltage-controlled (v.c.) nonlinear capacitors, current and voltage-controlled resistors, current-controlled (c.c.) nonlinear inductor, all four types of linear controlled sources and linear inductors, capacitors, resistors, independent sources.

The algorithm is to substitute all nonlinear elements in voltage controlled (c.u) or current controlled (c.i.) by ideal independent voltage sources respective ideal independent current sources and to replace the linear capacitors and inductors by resistive discrete circuit models associated with a numerical integration algorithm (for example, the backward Euler algorithm), in this way we obtain a linear and time-invariant circuit.

The method is more efficient if the analyzed circuit includes more linear circuit elements and controlled sources.

The hybrid equation notation, [8]:

$$\begin{aligned}
 X_{j+1}^{(m+1)} &= \begin{bmatrix} i_{u,j+1}^{(m+1)} \\ u_{i,j+1}^{(m+1)} \end{bmatrix}; \quad H = \begin{bmatrix} G_{u,u} & B_{u,i} \\ C_{i,u} & R_{i,i} \end{bmatrix}; \\
 x_{j+1}^{(m+1)} &= \begin{bmatrix} u_{u,j+1}^{(m+1)} \\ i_{i,j+1}^{(m+1)} \end{bmatrix}; \quad V_{j+1} = \begin{bmatrix} G_{u,e} & B_{u,j} \\ C_{i,e} & R_{i,j} \end{bmatrix} \begin{bmatrix} e_{j+1} \\ j_{j+1} \end{bmatrix} \\
 V_{LC,j} &= \begin{bmatrix} G_{u,L} & B_{u,C} \\ C_{i,L} & R_{i,C} \end{bmatrix} \begin{bmatrix} e_{L,j} \\ j_{C,j} \end{bmatrix} \quad X_{j+1}^{(m+1)} = Hx_{j+1}^{(m+1)} + V_{j+1} + V_{LC,j}, \quad (1)
 \end{aligned}$$

H represents the hybrid matrix of the circuit; V_{j+1} is the source vector corresponding to the independent voltage and current sources at the moment t_{j+1} , and $V_{LC,j}$ represents the source vector corresponding to the companion models of the linear inductors and capacitors, at the previous time moment

$B_{u,i}$ $C_{i,u}$ are the current (voltage) gain matrix of the tree-branch (link) v.c. (c.c.) nonlinear elements in respect of the link (tree-branch) c.c. (v.c.) nonlinear elements;, $u_{u,(j+1)}^{(m+1)}$ ($i_{i,(j+1)}^{(m+1)}$) the voltage (current) vector of the v.c. (c.c.) tree-branch

(link) nonlinear elements at the time moment $tj+1$, and the $(m+1)$ th iteration; j_{Cj} is the voltage (current) vector of the ideal independent voltage (current) sources from the companion schemes of the linear inductors (capacitors) at the time moment tj (or at the previous time steps).

The nonlinear resistor characteristics approximated by piecewise linear continuous curves have, for the time moment $tj+1$, and the $(k+1)$ th iteration:

for the v.c. nonlinear resistors:

$$\begin{aligned} i_{Ru,j+1}^{(m+1)} &= \hat{i}_{Ru}(u_{Ru,j+1}^{(m+1)}) = G_{du}(V_{j+1}^{(m)}) \cdot u_{Ru,j+1}^{(m+1)} + j_{Ru}(V_{j+1}^{(m)}), \\ u_{Ru}^-(V_{j+1}^{(k)}) &\leq u_{Ru,j+1}^{(m+1)} \leq u_{Ru}^+(V_{j+1}^{(m)}) \end{aligned} \quad (2)$$

for the c.c. nonlinear resistors

$$\begin{aligned} u_{Ri,j+1}^{(m+1)} &= \hat{u}_{Ri}(i_{Ri,j+1}^{(m+1)}) = R_{di}(V_{j+1}^{(m)}) \cdot i_{Ri,j+1}^{(m+1)} + e_{Ri}(V_{j+1}^{(m)}), \\ i_{Ri}^-(s_{j+1}^{(k)}) &\leq i_{Ri,j+1}^{(k+1)} \leq i_{Ri}^+(V_{j+1}^{(k)}) \end{aligned} \quad (3)$$

The current expression of a v.c. nonlinear capacitor, when its characteristic is approximated by piecewise linear continuous curve, for the time moment $tj+1$, and the $(k+1)$ th iteration (using the backward Euler integration algorithm):

$$\begin{aligned} i_{Cu,j+1}^{(k+1)} &= G_{dCu}(s_{j+1}^{(k)}) \cdot u_{Cu,j+1}^{(k+1)} + \hat{j}_{Cu,j+1} - j_{Cu,j} \\ G_{dCu}(s_{j+1}^{(k)}) &= \frac{C_{du}(s_{j+1}^{(k)})}{h}; \quad \hat{j}_{Cu,j+1} = \frac{Q_{Cu}(s_{j+1}^{(k)})}{h}; \quad j_{Cu,j} = \frac{q_{Cu,j}}{h}. \end{aligned} \quad (4)$$

The expression of a c.c. nonlinear coil, when its characteristic is approximated by piecewise linear continuous curve, for the time moment $tj+1$, and the $(k+1)$ th iteration (using the backward Euler integration algorithm):

$$\begin{aligned} u_{Li,j+1}^{(k+1)} &= R_{dLi}(s_{j+1}^{(k)}) \cdot i_{Li,j+1}^{(k+1)} + \hat{e}_{Li,j+1} - e_{Li,j}, \\ \text{Where } R_{dLi}(s_{j+1}^{(k)}) &= \frac{L_{di}(s_{j+1}^{(k)})}{h}; \quad \hat{e}_{Li,j+1} = \frac{\Phi_{Li}(s_{j+1}^{(k)})}{h}; \quad e_{Li,j} = \frac{\Phi_{Li,j}}{h}; \\ u_{Li,j+1}^{(k+1)} &= \frac{1}{h} [L_{di}(s_{j+1}^{(k)}) \cdot i_{Li,j+1}^{(k+1)} + \Phi_{Li}(s_{j+1}^{(k)}) - \Phi_{Li,j}] \end{aligned} \quad (5)$$

where $s_{j+1}^{(k)}$ is a certain segment at the moment $tj+1$ și iteration (k) , and parameters $L_{di}(s_{j+1}^{(k)})$ și $\Phi_{Li}(s_{j+1}^{(k)})$ represents the slope, the differential inductance, and the ordinate of origin of the corresponding segment $s_{j+1}^{(k)}$. The range $[i_{Li}^-(s_{j+1}^{(k)}) i_{Li}^+(s_{j+1}^{(k)})]$ represents the definition domain of the segment $s_{j+1}^{(k)}$.

Substituting the linear piecewise characteristics of the nonlinear circuit elements into the equations (1), we obtain:

$$\begin{aligned}
& \left[\begin{array}{cc} \mathbf{G}_{dvc}(s_{(k)}^{(j+1)}) - \mathbf{G}_{vc,vc} & -\mathbf{B}_{vc,cc} \\ -\mathbf{A}_{cc,vc} & \mathbf{R}_{dcc}(s_{(k)}^{(j+1)}) - \mathbf{R}_{cc,cc} \end{array} \right] \begin{bmatrix} \mathbf{v}_{vc(k+1)}^{(j+1)} \\ \mathbf{i}_{cc(k+1)}^{(j+1)} \end{bmatrix} = \\
& = V^{(j+1)} + V_{LC}^{(j)} + \begin{bmatrix} \hat{\mathbf{j}}_{vc}(s_{(k)}^{(j+1)}) \\ \hat{\mathbf{e}}_{cc}(s_{(k)}^{(j+1)}) \end{bmatrix} + \begin{bmatrix} \mathbf{j}_{vc}^{(j)} \\ \mathbf{e}_{cc}^{(j)} \end{bmatrix}.
\end{aligned} \tag{6}$$

The expression (6) contains $m=m_1+m_2$ independent equations in m_1 unknown voltages – voltage vector $\mathbf{v}_{vc(k+1)}^{(j+1)}$, and m_2 unknown currents – current vector $\mathbf{i}_{cc(k+1)}^{(j+1)}$, and is called the hybrid equation of the nonlinear circuit C .

The formulation algorithm for hybrid equations (1) for nonlinear analog circuits involves the following steps:

P1. Select a normal tree under the conditions mentioned above;

P2. First we replace all the nonlinear elements in voltage controlled (c.u) with equivalent ideal independent voltage sources, and the ones in current controlled (c.i.) with equivalent ideal independent current sources;

P3. Second we replace linear coils (magnetic coupled or not) and linear capacitors with equivalent resistive companion discrete circuits, (at appropriate time $= + h$, h being the integration step), associated with a default integration algorithm. Thereby, we obtain a linear resistive circuit which contain linear resistors (including the ones from the equivalent companion circuits of linear coils and capacitors), independent voltage and current sources (including those which have been substituted the nonlinear elements of circuit, c.u respectively c.i.) and controlled sources;

P4. It examines with a suitable simulation program the resistive circuit obtained in step P3. Thus obtaining the full symbolic expressions or numeric-symbolic of currents vector of nonlinear elements c.u, and the tensions of nonlinear elements c.i depending on: vector electromotive voltage (currents) of the ideal independent voltage source (current), depending on the vector electromotive tension (current) of equivalent companion schemes of linear coil (linear capacitors) and depending on the independent variables. From the expression vectors i_u and u^i is determined the hybrid matrix H , vector V_{j+1} source vector at the time determined by independent voltage and current sources and the vector V_{LC} - source vector corresponding to independent sources from discrete and resistive models, associated with a default integration algorithm of linear coils and capacitors;

The algorithm of formulation and solving hybrid equations can be used for a large class of nonlinear analog circuits.

The goal is to obtain a simple, flexible and general algorithm.

In order to calculate the symbolic expressions of the hybrid matrices we use the simulator Maple 15. Maple (developed by Maplesoft) is a math software

that is used to analyze, explore, visualize, and solve mathematical problems. Maple has a powerful math engine and covers also aspects of technical computing, including visualization, data analysis, matrix computation, and connectivity. This makes it the ideal tool for both education and research.

Some of the helpful functions in Maple: coeff - extract a coefficient of a polynomial, collect - collection after power coefficients, subs - substitute in an expression subexpression, read File- read a file and executes it.

At step 4, the suitable simulation program that examine the resistive circuit is Asinom.

We use the program Asinom to calculate the solution of the entire circuit (tension and current of all analyzed circuit). The two output files contains one the modified nodal-analysis equations and the other output file has the tensions and currents of all analyzed circuit.

3. Calculation of hybrid equations on analog circuits

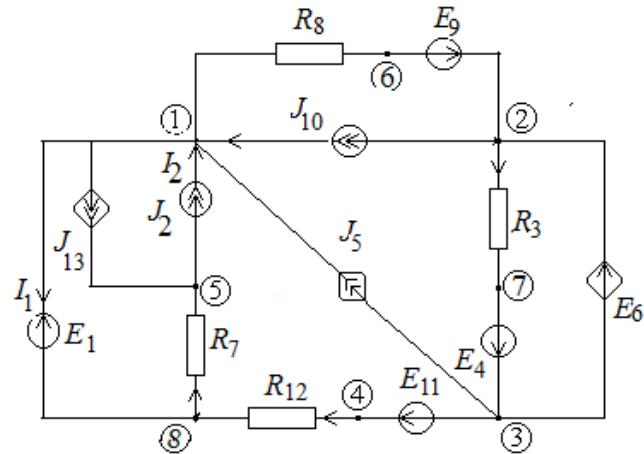


Fig. 1. Initial scheme of the circuit

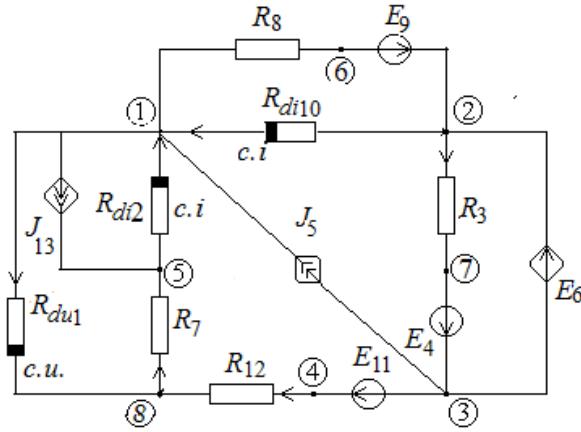


Fig. 2 equivalent circuit diagram of the Fig. 1, where nonlinear resistors c.i or c.u. are replaced with the ideal source equivalent current independent

The input file which describe the circuit in Fig.2:

13	7 3 E4	5 1 J2
8	3 2 e6(u) 8	1 5 j13(i) 3
1 6 R8	3 1 j5(u) 12	3 4 E11
6 2 E9	8 5 R7	4 8 R12
2 7 R3	8 1 U1	2 1 J10

Here are the Kirchhoff's equations for Fig. 2:

$+ (+G8)*V1 + (-G5_12)*V4 + (-G8)*V6 + (-1)*I8 + (+B13_3)*I3 = +J2+J10$ (**T1K→1**)
 $+ (-1)*I2 + (-1)*I5 + (+1)*I3 = -J10$ (**between nodes 3→4**)
 $+ (+G5_12)*V4 + (-1)*I4 + (+1)*I11 + (+1)*I5 = 0$ (**T1K→3**)
 $+ (+G12)*V4 + (-1)*I11 = 0$ (**T1K→4**)
 $+ (+G7)*V5 + (-B13_3)*I3 = -J2$ (**T1K→5**)
 $+ (-G8)*V1 + (+G8)*V6 + (+1)*I8 = 0$ (**between nodes 1→6**)
 $+ (+1)*I4 + (-1)*I3 = 0$ (**T1K→7**)
 $+ (-1)*V2 + (+1)*V6 = -E9$ (**between nodes 6→2**)
 $+ (-1)*V3 + (+1)*V7 = -E4$ (**between nodes 7→3**)
 $+ (-1)*V1 = -U1$ (**between nodes 1→8**)
 $+ (+1)*V3 + (-1)*V4 = -E11$ (**between nodes 3→4**)
 $+ (-A6_8)*V1 + (-1)*V2 + (+1)*V3 = 0$ (**T2K 3→2**)
 $+ (+G3)*V2 + (-G3)*V7 + (-1)*I3 = 0$ (**between nodes 2→7**)

Where $G8=1/R8$, $G12=1/R12$, $G7=1/R7$, $G5=1/R5$, $G8=1/R8$, $G3=1/R3$.

In order to calculate the symbolic expressions of the hybrid matrices we use the simulator Maple 15. The program developed below in Maple has a simple, flexible and general algorithm, see Appendix A

Numerical results of currents and voltage: $U1=515.99$, $I1=5.15$, $U2=-100$, $I2=5.15$, $U3=-24.78$, $I3=-0.12$, $U4=-20.0$, $I4=-0.12$, $U5=44.78$, $I5=4.71$, $E5=-44.78$, $U6=-371.21$, $I6=-3.8$, $J6=-3.87$, $U7=446.82$, $I7=4.46$, $U8=22.39$, $I8=-$

5.43, $U_9=-424.43$, $I_9=4.22$, $U_{10}=424.43$, $I_{10}=-0.24$, $J_{10}=-0.24$, $U_{11}=-200.00$, $I_{11}=-0.96$, $U_{12}=-193.60$, $I_{12}=-0.96$, $U_{13}=-415.99$, $I_{13}=10.00$

4. Analysis on Chua's nonlinear circuit

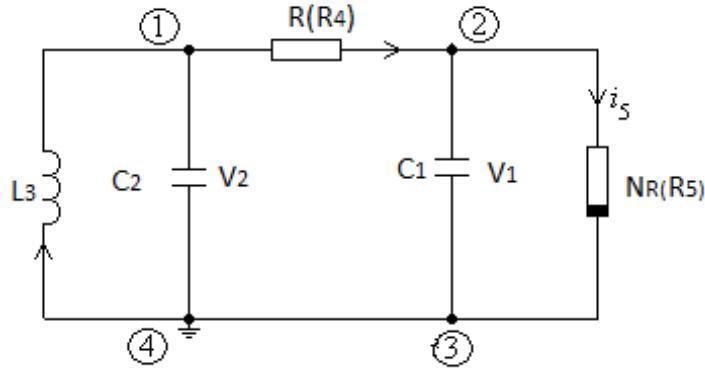


Fig. 3. Chua's nonlinear circuit

Chua's circuit consists of a linear inductor, a linear resistor, two linear capacitors and a single nonlinear resistor R_5 . The non-linear feature is a negative resistor type (all linear segments have a negative slope), which means it can not be implemented through a network that contains only resistors, inductors and capacitors [9-10].

Chua's circuit may be described by three ordinary differential equations

$$\begin{aligned}
 \frac{L di_L}{dt} &= -V_{C1} \\
 \frac{C_1 dV_{C1}}{dt} - i_L &= \frac{V_{C2} - V_{C1}}{R} \\
 \frac{C_2 dV_{C2}}{dt} - \frac{V_{C2} - V_{C1}}{R} &= g(V_{C2})
 \end{aligned} \tag{7}$$

It is noted that it was considered as state variables i_L , V_{C1} and V_{C2} [4-6].

Interpretations of the three state equations are [6-7]:

1. The voltage on the inductance coil L is equal to the voltage on capacitor $C1$ considered opposite.
2. The current through capacitor $C1$ is equal to the difference between the current through coil and the current through resistor $R4$.

3. The current through capacitor C_2 is equal to the difference between the current through resistor R_4 and current through non-linear element R_5 .

The Fourier analysis of Chua's circuit using Matlab. In order to review the nonlinearity of Chua's circuit, a Fourier analysis was performed in Matlab as presented in the images below:

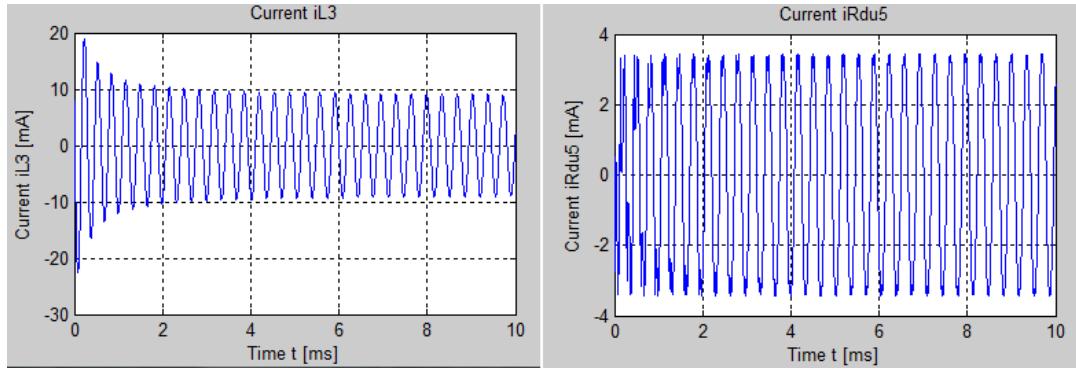


Fig. 4. The current i_{L3} and current i_{R5} of Chua's circuit

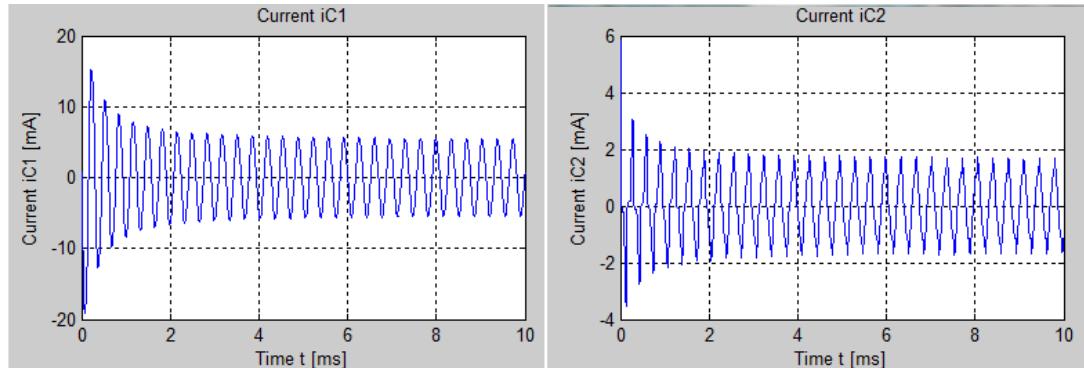


Fig. 5. The current i_{C1} and current i_{C2} of Chua's circuit

In Fig. 6, it can be observed the behavior associated with a periodic oscillator with an unstable solution, characterized by a trajectory attracted to the limit cycle.

Afterwards, a large outer limit cycle is presented in Fig.7 and Fig.8 corresponding to the outer segments of Chua's diode's characteristics known as the boundary crisis where the active resistor becomes eventually passive.

The simulation results support the fact that the Chua circuit is an oscillator with a complex behavior, characterized by state bifurcations and a tendency towards chaotic behavior.

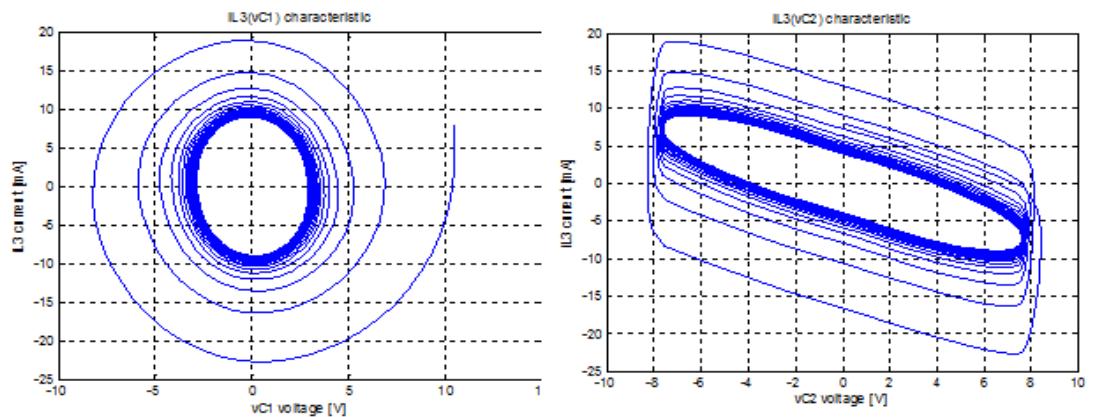


Fig. 6. The $iL3(vC1)$ characteristic and $iL3(vC2)$ characteristic of Chua's circuit

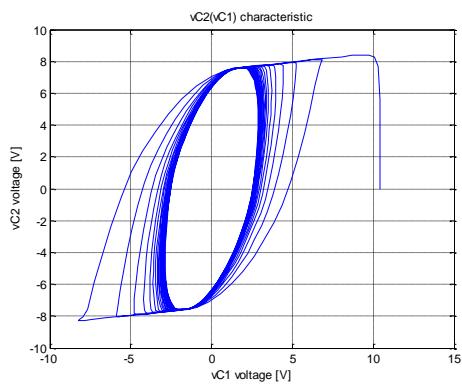


Fig. 7. The $vC2(vC1)$ characteristic of Chua's circuit

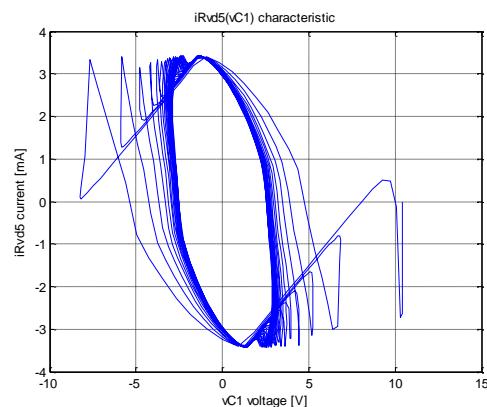


Fig. 8. The $iRvd5(vC1)$ characteristic of Chua's circuit

5. Conclusions

This paper presents new approaches of analyzing the nonlinear resistive circuit analysis. The hybrid analysis method of the nonlinear analog circuits presents the advantage of separation between the linear and the nonlinear part. This allows the computation only once at the beginning of the iteration process, of those parts of the circuit equations that exclusively depends on the parameters of the linear terms. A significant efficiency in circuit analysis and an improvement of the accuracy in the numerical calculations are obtained by combining this hybrid procedure with a very efficient implicit integration algorithm, in which only the symbols of the parameters corresponding to the nonlinear circuit elements are considered. Also, the dynamic elements are replaced by discrete resistive models associated with an implicit numerical integration algorithm.

Another advantage of the hybrid-method in comparison with nodal-analysis method is flexibility by allowing the nonlinear elements to be either voltage-controlled (c.v.) or current-controlled (c.c.).

Appendix A

Program developed in Maple that calculates the symbolic expressions of the hybrid matrices is showed in Fig. 9.

This algorithm can be used for different circuits and the only thing that must be change is the input file.

- Open the file and read the data from it
- Import the solution from Asinom program
- Compute the covariable vector
- Compute the hybrid matrix H
- Compute the hybrid matrix V_{j+1} is the source vector corresponding to the independent voltage and current sources at the moment t_{j+1} , and V_{LC} represents the source vector corresponding to the companion models of the linear inductors and capacitors at the previous time moment
- Numerical calculation of all the matrix used H , V_{ej} , V_{LC}
- Calculate the expressions of characteristic of non-linear element parameter, linearized on portions, corresponding to the combination of certain segments s and hybrid equations

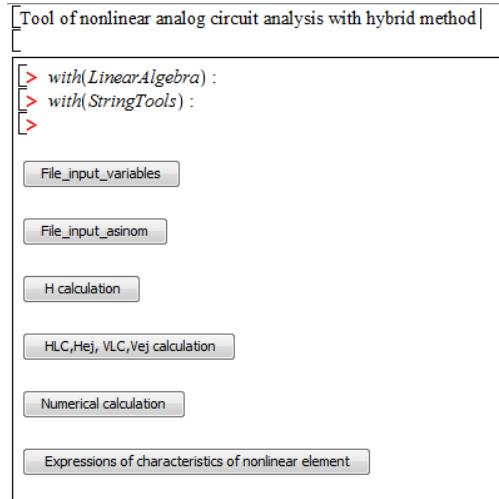


Fig. 9. The program to calculate hybrid matrix in Maple

Compute the covariable vector result:

$$\begin{aligned} & \text{table}\left(\left[1 = \frac{(A6_8 + 1 + G5_12R12 + A6_8G5_12R12) U1}{G5_12R8R12 + R12 + R8} + \frac{R12 I10}{G5_12R8R12 + R12 + R8} \right. \right. \\ & + \frac{(G5_12R12 + 1) E9}{G5_12R8R12 + R12 + R8} + \frac{(G5_12R12 + 1) E11}{G5_12R8R12 + R12 + R8}, 2 = -1.E9, 3 = \\ & \left. \left. 1.(-1.R3G7 - 1.B13_3A6_8) U1 + \frac{1.I2}{G7} - \frac{1.B13_3E4}{R3G7}\right]\right) \end{aligned}$$

Hybrid matrix H

$$\begin{aligned} & \text{print}(H) \\ & \text{table}\left(\left[1 = \text{table}\left(\left[1 = \frac{A6_8 + 1 + G5_12R12 + A6_8G5_12R12}{G5_12R8R12 + R12 + R8}, 2 = 0, 3 = \right. \right. \right. \right. \\ & \left. \left. \left. \left. \frac{R12}{G5_12R8R12 + R12 + R8}\right], 2 = \text{table}([1 = 0, 2 = 0, 3 = 0]), 3 = \text{table}\left(\left[1 = \right. \right. \right. \right. \\ & \left. \left. \left. \left. 1.(-1.R3G7 - 1.B13_3A6_8), 2 = \frac{1.}{G7}, 3 = 0\right]\right]\right) \end{aligned}$$

Calculate the expressions of characteristic of non-linear element parameter, linearized on portions, corresponding to the combination of certain segments s and hybrid equations:

$$\begin{aligned}
 > & \text{evalm}(B) = \text{evalm}(TL) \\
 & [(GdI(s) + 0.004285714286) uul - 0.1428571429 ii10, Rdi2(s) ii2, 1.000000000 uul - 100. ii2 \\
 & + Rdi10(s) ii10] = \begin{bmatrix} 1.285714286 - jul(s) \\ -200.0 - ei2(s) \\ -200.0000000 - ei10(s) \end{bmatrix} \\
 >
 \end{aligned}$$

R E F E R E N C E S

- [1] *M. Iordache, Lucia Dumitriu*, Computer Aided Simulation of Analog Circuits – Algorithms and Computational Techniques (in RO), Publisher POLITEHNICA Press, Bucharest 2014, Vol:2
- [2] *M. Iordache*, Symbolic and Numeric Simulation of Analog Circuits – User Guides, MATRIX ROM, Bucureşti, 2015
- [3] *Kiyotaka Yamamura, Mitsuru Tonokura*, Formulating hybrid equations and state equations for nonlinear circuits using SPICE, article in International Journal of Circuit Theory and Applications, October 2011
- [4] *L., O., Chua, and P., M., Lin*, Computer-Aided Analysis of Electronic Circuits: Algorithms and Computational Techniques, Englewood cliffs, NJ: Prentice-Hall 1975.
- [5] *B. J. Leon, D. J. Shaefer*, "Volterra series and Picard iteration for nonlinear circuits and systems", IEEE Trans. on Circuits and Systems, Vol. CAS-25, Sept. 1978, pp. 789–793
- [6] *D. Yu, C. Zheng, H. H.-C. Iu, T. Fernando, and L. O. Chua*, "A new circuit for emulating memristors using inductive coupling," IEEE Access, vol. 5, pp. 1284–1295, 2017.
- [7] *A. Ushida, L. O. Chua*, "Frequency-domain analysis of nonlinear circuits driven by multi-tone signals", IEEE Trans. on Circuits and Systems, Vol. CAS-31, No. 9, 1984, pp. 766-778
- [8] *D. Sanatescu, L. Ene, M. Iordache*, "Research on analysis of analog circuits with hybrid method", IEEE, 10.1109/ICATE.2016.7754601, 2016
- [9] *Chua, L. O.*, Memristor - the missing circuit element. IEEE Trans. Circuit Theory, Vol. 18, no. 5 507–519 (1971).
- [10] *Abdullah G. Alharbi, Mohammed E. Fouda, Zainulabideen J. Khalifa, Masud H. Chowdhury*, "Electrical Nonlinearity Emulation Technique for Current-Controlled Memristive Devices", Access IEEE, vol. 5, pp. 5399-5409, 2017